

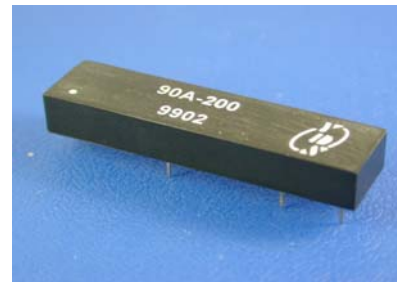


FEATURES

- INHERENT DELAY:15ns±1ns
- INPUT SIGNAL REQUIREMENTS:TTL LOGIC.
- OUTPUT FAN-OUT:TTL SCHOTTKY LOAD
- TOTAL DELAY TOLERANCE:±5% OR ±1ns, WIG

ELECTRICAL CHARACTERISTICS

I _{IH} Logic"1" Input Current	:50uA max
I _{IL} Logic"0" Input Current	:-2uA max
V _{OH} Logic"1" Output Voltage	:2.4V min
V _{OL} Logic"0" Output Voltage	:0.4V max
V _{IH} Logic"1" Input Voltage	:2.0V min
V _{IL} Logic"0" Input Voltage	:0.8V max
TA Operating Temperature	:0°C to 70°C
NH Fanout"1" Output	:20 TTL Load
NL Fanout"0" Output	:10 TTL Load
Supply Current	:170mA MAX.



INPUT PULSE TEST CONDITION

Pulse Voltage	:3.0V
Pulse Width	:3Td
Pulse Spacing	:1000ns
Pulse Rise Time	:2nS(0.75V to 2.4V)
Time Delay Measured	:@1.5V level
Supply Voltage V _{cc}	:5.0±0.25Vdc

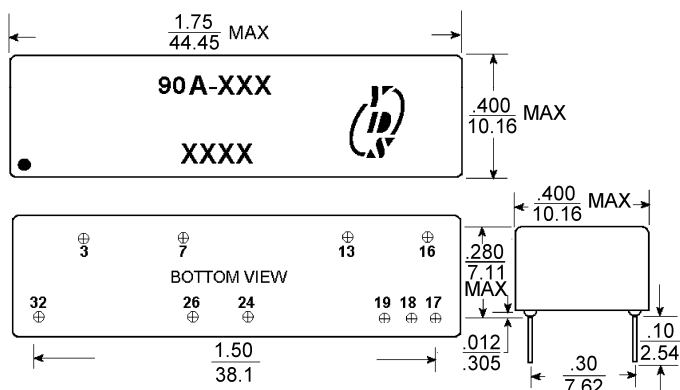
ELECTRICAL SPECIFICATIONS

PART NUMBER	INCREMENTAL DELAY PER STEP(ns)	TOTAL DELAY CHANGE(ns)
90A-015	1	15±1
90A-030	2	30±1.5
90A-045	3	45±2.2
90A-060	4	60±3
90A-075	5	75±3.7
90A-090	6	90±4.5
90A-120	8	120±6
90A-150	10	150±7.5
90A-180	12	180±9
90A-225	15	225±11.2
90A-300	20	300±15
90A-375	25	375±18.7
90A-450	30	450±22.5
90A-525	35	525±26.2
90A-600	40	600±30
90A-675	45	675±22.7
90A-750	50	750±37.5
90A-900	60	900±45
90A-1200	80	1200±60
90A-1500	100	1500±75

FUNCTION TABLE

ENABLE	ADDRESS(BIT NO.)					DEAY
L	L	L	L	L	L	T0
L	L	L	L	L	H	T1
L	L	L	L	H	L	T2
L	L	L	L	H	H	T3
L	L	L	H	L	L	T4
L	L	L	H	L	H	T5
L	L	L	H	H	L	T6
L	L	L	H	H	H	T7
L	L	H	L	L	L	T8
L	L	H	L	L	H	T9
L	L	H	L	H	L	T10
L	L	H	L	H	H	T11
L	L	H	H	L	L	T12
L	L	H	H	L	L	T13
L	L	H	H	H	L	T14
L	L	H	H	H	H	T15
H	X	X	X	X	X	L

MARKINGS AND DIMENSIONS



Dimensions:inches/mm

Unless otherwise specified, all tolerances are ±.010/±0.25

PIN CONNECTIONS

