



### FEATURES

- STANDARD 16-PIN PACKAGE.
- 5 EQUALLY SPACED DELAY TAPS.
- ECL 10K INTERFACED.
- DELAYS FROM 10ns-500ns
- POWER DISSIPATION: 200mW TYP.(NO LOAD)

### ELECTRICAL CHARACTERISTICS

I <sub>IH</sub> Logic"1" Input Current	:265uA max
I <sub>IL</sub> Logic"0" Input Current	:-5uA max
V <sub>OH</sub> Logic"1" Output Voltage	:-0.96V min
V <sub>OL</sub> Logic"0" Output Voltage	:-1.65V max
V <sub>IH</sub> Logic"1" Input Voltage	:-0.98V min
V <sub>IL</sub> Logic"0" Input Voltage	:-1.63V max
TA Operating Temperature	:0°C to 70°C
Supply Current	:65mA Typ.

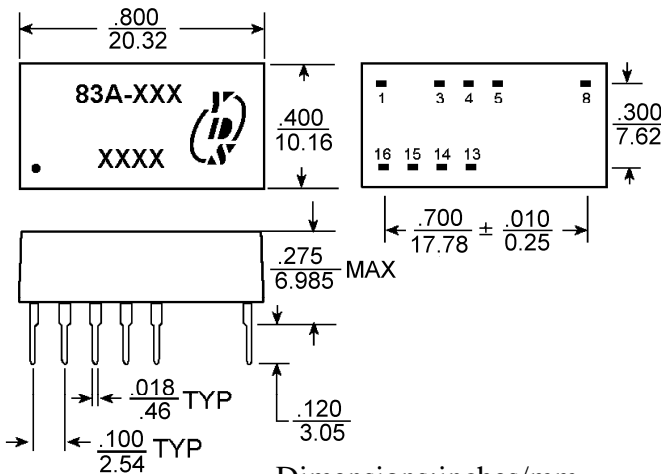
### INPUT PULSE TEST CONDITION

Pulse Voltage	:-1V(-0.75V TO -1.75V)
Pulse Width	:3Td
Pulse Spacing	:10Td
Pulse Rise Time	:2nS(20% to 80%)
Time Delay Measured	:@-1.3V level
Supply Voltage V <sub>cc</sub>	:-0V
Supply Voltage V <sub>ee</sub>	:-5.2Vdc

### ELECTRICAL SPECIFICATIONS

PART NO.	TOTAL DELAY (nS)	TAP DELAY (nS)	MAX OUTPUT RISETIME (nS)	MAX OUTPUT FALLTIME (nS)
83A-010	10±1	2.0	4.0	4.0
83A-020	20±1	4.0	4.0	4.0
83A-025	25±1.2	5.0	4.0	4.0
83A-050	50±2.5	10.0	4.0	4.0
83A-075	75±3.7	15.0	5.0	5.0
83A-100	100±5	20.0	6.0	6.0
83A-150	150±7.5	30.0	7.0	7.0
83A-200	200±10	40.0	8.0	8.0
83A-250	250±12.5	50.0	10.0	10.0
83A-300	300±15	60.0	15.0	15.0
83A-400	400±20	80.0	15.0	15.0
83A-500	500±25	100.0	20.0	20.0

### MARKINGS AND DIMENSIONS



Dimensions: inches/mm

Unless otherwise specified, all tolerances are ±0.10/±0.25

### PIN CONNECTIONS

